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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/091,426	03/07/2002	Hiroshi Arai	H-1033	8886
24956	7590 07/22/2004		EXAM	INER
	LY, STANGER & MA ONAL ROAD	ZARNEKE, DAVID A		
SUITE 370	ONAL ROAD	ART UNIT	PAPER NUMBER	
ALEXAND	RIA, VA 22314		2827	
			DATE MAILED: 07/22/2004	4

Please find below and/or attached an Office communication concerning this application or proceeding.

	······································	Applicat	ion No.	Applicant(s)				
Office Action Summary		10/091,4	426	ARAI ET AL.				
		Examine		Art Unit				
		David A.	Zarneke	2827				
Period fo	The MAILING DATE of this communicati or Reply	on appears on th	ne cover sheet with	the correspondence a	ddress			
THE - Exte after - If the - If NC - Failt Any	ORTENED STATUTORY PERIOD FOR MAILING DATE OF THIS COMMUNICAT nsions of time may be available under the provisions of 37 SIX (6) MONTHS from the mailing date of this communicate operiod for reply specified above is less than thirty (30) day operiod for reply is specified above, the maximum statutor reto reply within the set or extended period for reply will, be reply received by the Office later than three months after the patent term adjustment. See 37 CFR 1.704(b).	FION. CFR 1.136(a). In no e tion. ys, a reply within the stay period will apply and yoy statute, cause the ap	event, however, may a replatutory minimum of thirty (will expire SIX (6) MONTHOPLICATION TO BEAR	ly be timely filed (30) days will be considered time (35) from the mailing date of this (35) NDONED (35) U.S.C. § 133).				
Status								
1)⊠	Responsive to communication(s) filed on 25 May 2004.							
2a) <u></u> ☐	This action is FINAL . 2b)	This action is	non-final.					
3)□	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.							
Disposit	ion of Claims							
_	Claim(s) is/are objected to.							
Applicat	on Papers							
	The specification is objected to by the Ex							
10)	☐ The drawing(s) filed on is/are: a)☐ accepted or b)☐ objected to by the Examiner.							
	Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).							
11)	Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d). The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.							
Priority (ınder 35 U.S.C. § 119							
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 								
Attachmen	t(s)							
	e of References Cited (PTO-892)			mmary (PTO-413)				
3) 🔲 Infon	e of Draftsperson's Patent Drawing Review (PTO-9 nation Disclosure Statement(s) (PTO-1449 or PTO r No(s)/Mail Date			Mail Date ormal Patent Application (PT .	O-152)			

Art Unit: 2827

DETAILED ACTION

Election/Restrictions

This application contains claims directed to the following patentably distinct species of the claimed invention:

- 1) wherein the resin sealing step for the semiconductor chip is carried out in a state in which, in a section orthogonal to a second side of the main surface of the semiconductor chip which intersects the first side of the main surface of the semiconductor chip, a sectional area of an area between the main surface of the wiring substrate and the main surface of the cavity at a position outside a side face along the extending direction of the second side of the main surface of the semiconductor chip is smaller than a sectional area of an area between the main surface of the semiconductor chip and the main surface of the cavity; and
- 2) wherein the resin sealing step for the semiconductor chip carried out state in which the distance from a side face extending along the third side the main surface of the semiconductor chip to a side face along the third side of the main surface of longer than the distance from the second side extending the cavity side face extending along main surface the semiconductor chip to extending along the second side of the main surface of the cavity and is longer than the distance from a side face extending along the fourth side of the main surface the semiconductor chip to side face side face extending along the fourth side of the main surface of the cavity.

Applicant is required under 35 U.S.C. 121 to elect a single disclosed species for prosecution on the merits to which the claims shall be restricted if no generic claim is finally held to be allowable. Currently, no claim is generic.

Applicant is advised that a reply to this requirement must include an identification of the species that is elected consonant with this requirement, and a listing of all claims readable thereon, including any claims subsequently added. An argument that a claim is allowable or that all claims are generic is considered nonresponsive unless accompanied by an election.

Upon the allowance of a generic claim, applicant will be entitled to consideration of claims to additional species which are written in dependent form or otherwise include all the limitations of an allowed generic claim as provided by 37 CFR 1.141. If claims are added after the election, applicant must indicate which are readable upon the elected species. MPEP § 809.02(a).

Should applicant traverse on the ground that the species are not patentably distinct, applicant should submit evidence or identify such evidence now of record showing the species to be obvious variants or clearly admit on the record that this is the case. In either instance, if the examiner finds one of the inventions unpatentable over the prior art, the evidence or admission may be used in a rejection under 35 U.S.C. 103(a) of the other invention.

Applicant is advised that the reply to this requirement to be complete must include an election of the invention to be examined even though the requirement be traversed (37 CFR 1.143).

Applicant is reminded that upon the cancellation of claims to a non-elected invention, the inventorship must be amended in compliance with 37 CFR 1.48(b) if one or more of the currently named inventors is no longer an inventor of at least one claim remaining in the application. Any amendment of inventorship must be accompanied by a request under 37 CFR 1.48(b) and by the fee required under 37 CFR 1.17(i).

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to David A. Zarneke whose telephone number is (571)-272-1937. The examiner can normally be reached on M-F 10 AM-6PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kamand Cuneo can be reached on (571)-272-1957. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Zarneke /

DAVID ZARNE Primary Examiner PRIMARY EXAMINER

July 17, 2004